

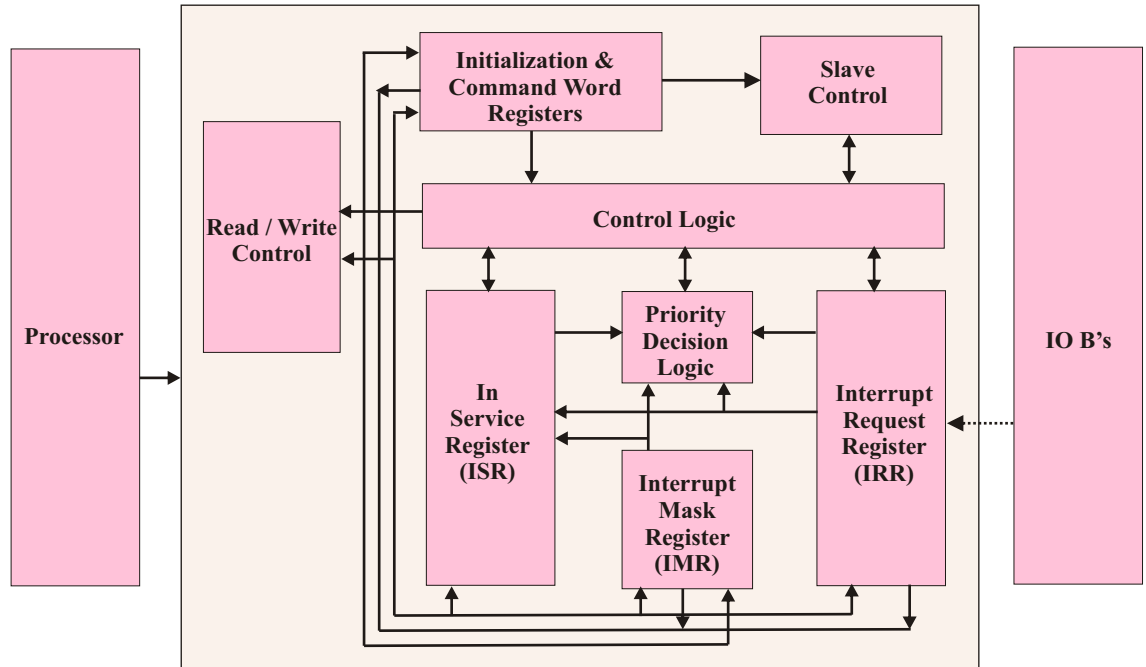
Overview

iW-Interrupt Controller is designed to transfer the interrupt with highest priority to the CPU, along with interrupt address information.

Features

- ❖ Eight interrupt request input per chip
- ❖ Up to 64 interrupt request inputs per system
- ❖ Edge or level triggered interrupt request inputs
- ❖ Individually maskable interrupt requests
- ❖ Programmable interrupt request priority orders
- ❖ Polling operation capability
- ❖ Extended mode with cascade connection of external interrupts
- ❖ Supports Slave mode in extended mode

Block Diagram



Core Benefits

- ❖ Can be used to assign priority levels to interrupt outputs
- ❖ Allows cascading of multiple interrupts

Deliverables

- ❖ Technical Specification
- ❖ RTL Verilog Synthesizable Code
- ❖ Comprehensive Test Environment
- ❖ Technical Support and Maintenance

About Us

iWave Systems Technologies is an embedded Hardware and Software Turnkey Design Services focused on providing integrated solutions for developing innovative products and systems in the areas of Communication, Consumer Electronics and Multimedia. iWave offers complete turnkey solutions for systems engineering and product development.

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